



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Soichi Kobayashi et al.

Application No.: 10/632,928

Filing Date: August 4, 2003

Title: SEMICONDUCTOR INTEGRATED
CIRCUIT CAPABLE OF TESTING
WITH SMALL SCALE CIRCUIT
CONFIGURATION

Group Art Unit: 2138

Examiner: SAQIB JAVAID SIDDIQUI

Confirmation No.: 4942

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: December 12, 2006

Sir:

The following extension of time is requested to: the Office Action dated July 14, 2006 for

Two Months to December 14, 2006

☒ \$ 450

☐ \$ 225

☐ The shortened statutory period has been reset by an Advisory Action dated _____.

☐ An Extension fee in the amount of _____ is enclosed.

☐ Charge _____ to Deposit Account No. 02-4800.

☒ Charge \$ 450 to credit card. Form PTO-2038 is attached.

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted;

BUCHANAN INGERSOLL & ROONEY PC

Date: December 12, 2006

By:

Ellen Marcie Emas
Registration No. 32131

12/13/2006 JADD01 00000000 10632928

01 FC:1252

450.00 0P

P.O. Box 1404
Alexandria, VA 22313-1404
703 836 6620